

A multiple antenna transceiver RFIC has been designed and fabricated in a SiGe BiCMOS process for 5GHz OFDM transmission. The fully-integrated chip includes 5 antenna inputs, 2 receiver chains, one transmit chain, a PLL, as well as a serial digital interface. Rx NF is 4.5dB, Tx Psat is 18dBm, and phase jitter is 1degRMS.