

Radio Control

AGC DAC

Radio Addr = 4

0 DAC Bit

1

2

3

4

5

6

7

8

9

10

11

12

MSB
DAC
#1

B8

B7

B6

B5

B4

B3

B2

B1

LSB
DAC

B5

B4

B3

B2

B1

← shifted out first

MSB of this 8 bit DAC word

MSB of this 5 bit DAC word

FSI

Address = 5

Main PLL

(word 1)

Motorola

158 chip

(word 2)

0

FSIN (MSB)

1

2

3

4

5

6

7

8

9

FSIN (LSB)

10

FSIR (MSB)

11

12

13

14

15

16

FSIA (LSB)

17

"0" Control Bit

0

FSIR (MSB)

1

2

3

4

5

6

7

8

9

10

11

12

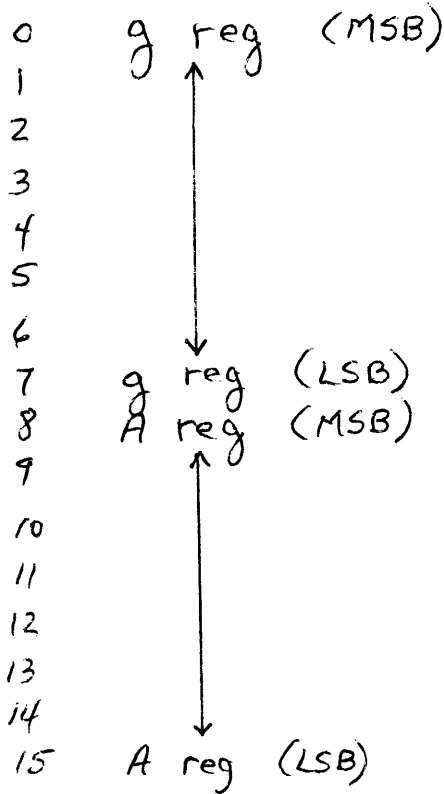
13

14

FSIR (LSB)

"1" Control bit

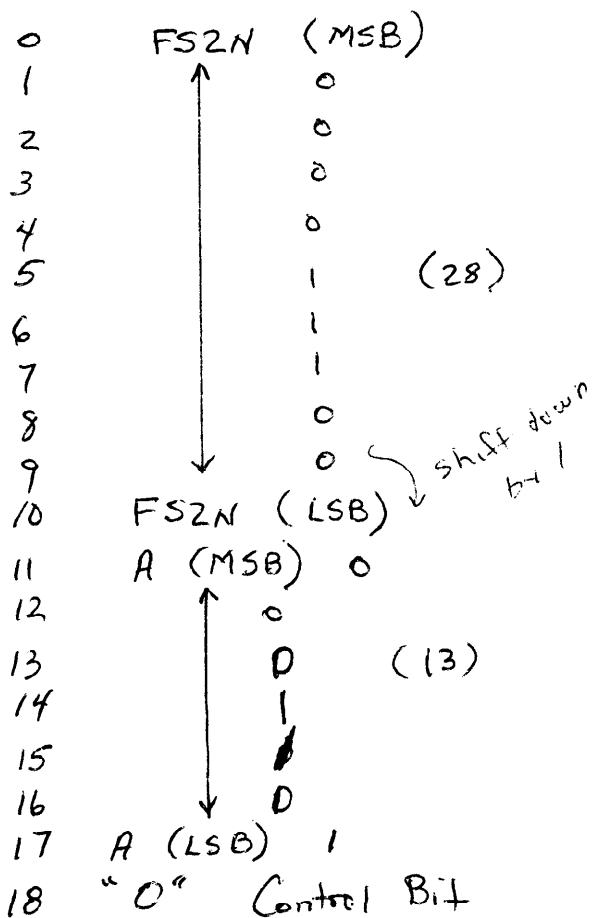
Actel Register Addr = 7



FS2 (45.45 MHz)

Fujitsu MB1505

Addr = 6



M value 1 (32/33)

R (MSB) D

2 D

3 0

4 6

5 0

6 1

7 1

8 0

9 0

10 1

11 0

12 1

13 0

14 R (LSB)

15 "1" Control Bit

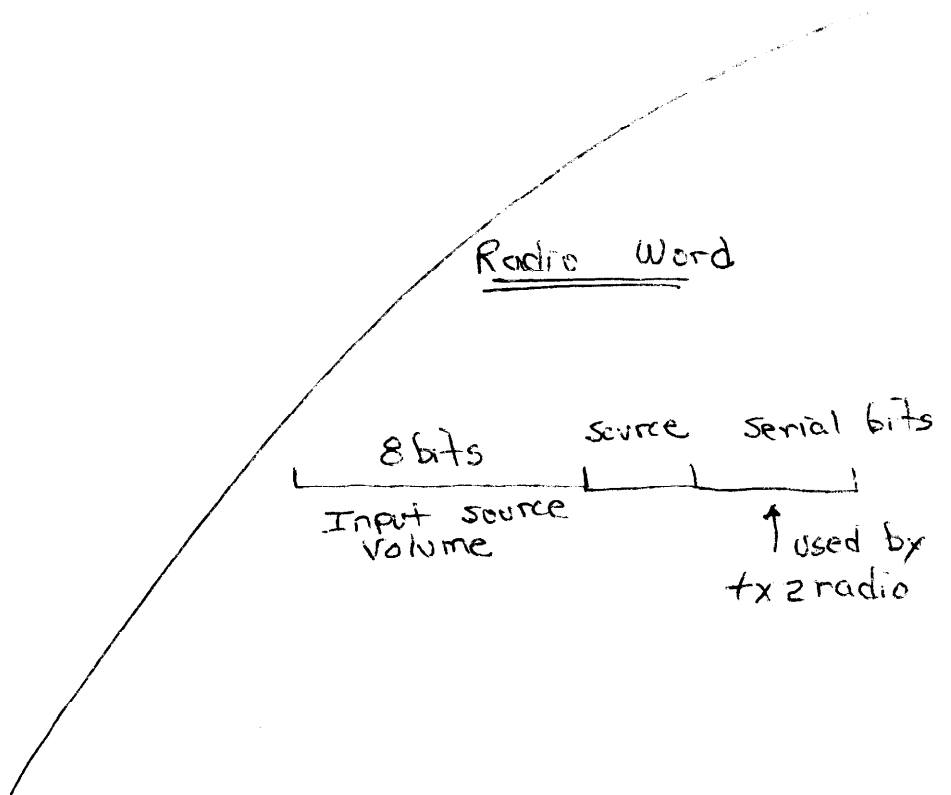
(7.202)

Control word 1 Addr = 2

- | | |
|----|---------------|
| 0 | AFC DAC (MSB) |
| 1 | |
| 2 | |
| 3 | |
| 4 | |
| 5 | |
| 6 | |
| 7 | AFC DAC (LSB) |
| 8 | DIVMODE2 |
| 9 | DIVMODE1 |
| 10 | OI * |
| 11 | OI |
| 12 | VC2 |
| 13 | VC1 |
| 14 | Cerfil out * |
| 15 | Cerfil out |

Control word 2 Addr = 3

- | | |
|----|---------|
| 0 | - |
| 1 | 7 |
| 2 | 6 |
| 3 | 5 |
| 4 | 4 |
| 5 | 3 |
| 6 | 2 |
| 7 | 1 |
| 8 | 11 |
| 9 | 10 |
| 10 | 9 |
| 11 | Bndsel8 |
| 12 | VLNAON |
| 13 | PADON |
| 14 | IFSEL |
| 15 | HLNAON |



15 Nov. 93

DSP

tx2 radio

tx adrs
tx msbs
tx lsbs
tx lng

port address (serial)
16 msbs of data
" lsbs
of data bits

Shifted out (st)

↓
MSB

0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15

0 0 1 1 0 1 0 1 1 0 1 1 x x x

← example of
a 13 bit word
to be sent out