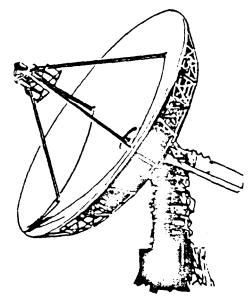
The "Art" of Phase Noise Measurement

Dieter Scherer May 1983

RF & Microwave Measurement Symposium and Exhibition





INTRODUCTION

Measuring and specifying phase noise has become increasingly important as phase noise is the limiting factor in many RF and microwave systems, like Doppler radar and space telemetry systems or communication links. The complexity and subtleties of the measurement have earned it the reputation of being more art than science.

This paper gives an introduction to phase noise measurement focusing on the two most common and useful techniques, the "Two Source Phase Detector" and the "Delay Line Frequency Discriminator" methods. System limitations are pointed out as well as the potential sources of erroneous data when phase noise is measured in a bench set up.

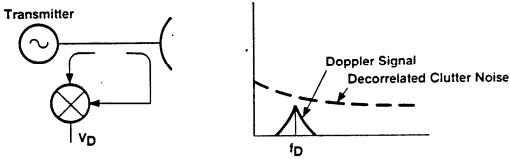
The 11729B Phase Noise Down Converter is HP's latest contribution in this field, designed to simplify and automate the complex task. A table of comparisons will show where the 11729B and the various other HP solutions are optimally employed.

1. Why Measure Phase Noise?

Phase noise causes . . .



• Lower receiver sensitivity in multi-signal environment



IF Bandwidth

Clutter noise in Doppler Radar Systems
 Digital Modulation
 Demodulator

Phase errors in digital communication systems

Phase noise imposes fundamental limitations wherever a weak signal is processed in the presence of a strong interfering signal. In the above example, phase noise sidebands of the receiver local oscillator are transferred to the IF product of the strong interfering signal and cover up the weak wanted signal.

A similar situation exists with coherent Doppler radar. The strong interferring signal in this case is produced by reflections from large stationary objects. Phase noise side bands of this unwanted return signal are decorrelated by delay and potentially cover as clutter noise the weak Doppler signal.

Even in the world of digital data transmission phase noise is a limiting factor. Phase noise adds to overall system noise increasing Bit Error Rate and may cause a cycle slip in the carrier or data clock recovery.

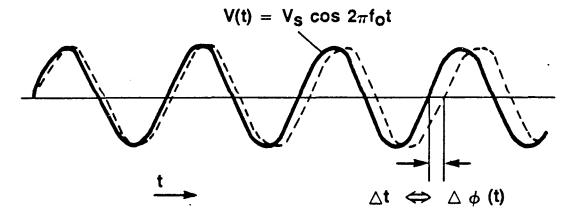
2. Basic Representation of Phase Noise

2.1 In the Time Domain:

 $v(t) = Signal with random phase fluctuation <math>\Delta \phi$ (t)

$$v(t) = V_{S} \cos \left[2\pi f_{O} t + \Delta \phi(t)\right]$$

.Oscilloscope Display



Frequency and phase are related by

$$f(t) = \frac{1}{2\pi} \frac{d \phi(t)}{dt}$$

In the time domain phase noise can be observed as phase jitter of the signal on an oscilloscope display or a time interval counter may detect the instantaneous time fluctuations of the zero crossings.

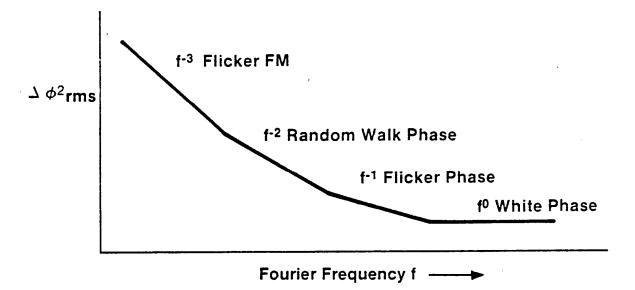
Phase or frequency fluctuations are the same physical phenomena. One can be derived from the other as angular frequency is the first derivative of phase with respect to time.

2.2 In the Frequency Domain:

$$F \Delta \phi (t) = \Delta \phi (f)$$

Spectral Density of Phase Fluctuations $S_{\Delta\phi}$

$$S_{\Delta \phi}(f) = \Delta \phi^2_{rms}(f)$$



Spectral Density of Frequency Fluctuations S 4 f

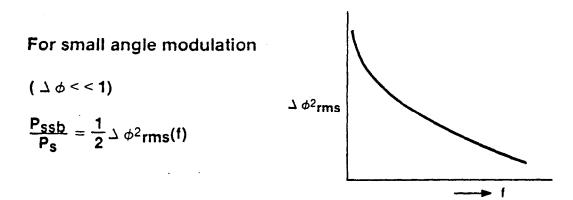
$$S_{\Delta} f(f) = \Delta f^2_{rms}(f) = f^2 \Delta \phi^2_{rms}(f) = f^2 S_{\Delta} \phi(f)$$

Spectral density distributions describe frequency or phase noise in the frequency domain. Terms like "White," "Flicker" and "Random Walk" refer to the slope of f°, f-1, and f-2 of the density distribution.

Multiplication by the Fourier frequency — corresponding to differentiation in the time domain — converts the spectral density of phase noise to the spectral density of frequency noise.

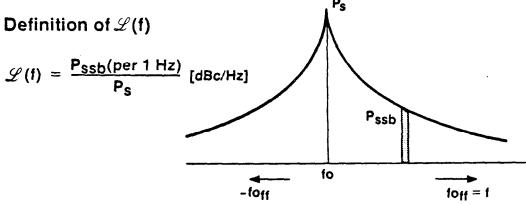
2.3 Relation of Phase Noise to Carrier Sideband Noise

The modulation of the signal phase manifests itself in modulation sidebands to the carrier at offset frequencies which are multiples of the modulation rate (Fourier frequency). The sideband levels are related to the magnitude of the phase deviation by Bessel terms. For small-angle modulation, only the first Bessel term is significant and the relation between phase deviation and sideband level is approximated by



A common, though indirect, representation of phase noise is $\mathcal{L}(f)$, the ratio of the single sideband noise power in a 1 Hz bandwidth to the total carrier power specified at a given offset f of the carrier.

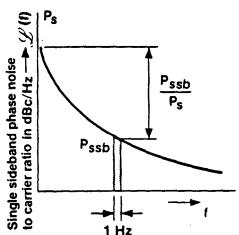
This representation is applicable only for very small phase deviations, sufficiently small to produce negligible higher order sidebands.



Relation between \mathcal{L} , $S_{\Delta \phi}$, $S_{\Delta f}$:

for $\Delta \phi << 1$

$$\mathcal{L}(\mathfrak{f}) = \frac{1}{2} s_{\Delta \phi}(\mathfrak{f}) = \frac{1}{2} \frac{1}{\mathfrak{f}^2} s_{\Delta \mathfrak{f}}(\mathfrak{f})$$



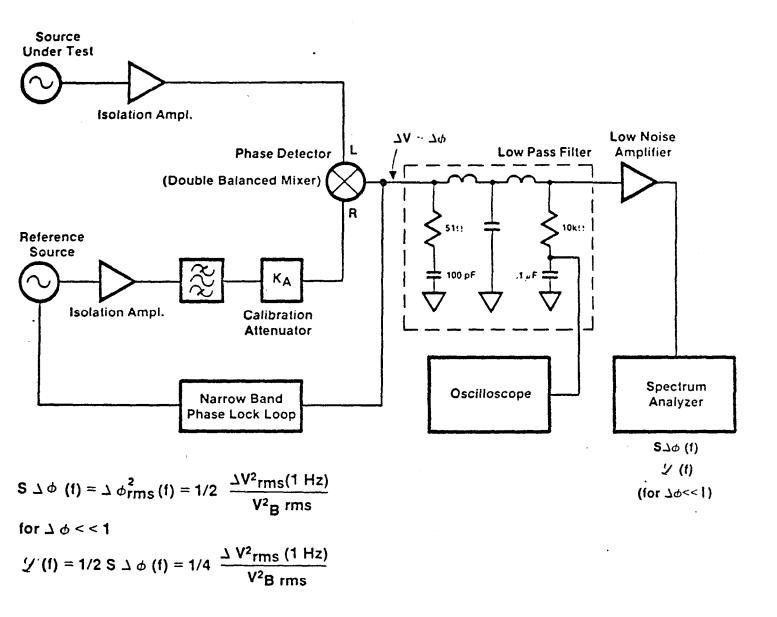
3. Phase Noise Measurement Techniques

- Direct Spectrum Analysis
- ♦ Two Sources and Phase Detector
 - Frequency Discriminator
- Delay Line Frequency Discriminator
 - Carrier Suppression (Ondria Bridge)
 - Heterodyne Period Counter

From the multitude of techniques to measure phase or frequency noise two basic methods are singled or and explained in some detail on the following pages: the "Two Sources and Phase Detector" technique an the "Delay Line Frequency Discriminator" technique.

Both methods are relatively simple to implement and are broadband solutions. They also complimer each other well. The first method measures phase noise; high sensitivity is obtainable and two sources ar needed. The second method measures frequency noise, has inherently lower sensitivity close in but n second source is required.

3.1 Two Sources and Phase Detector Technique

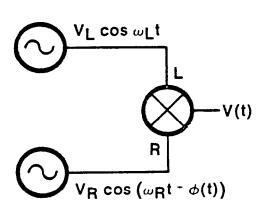


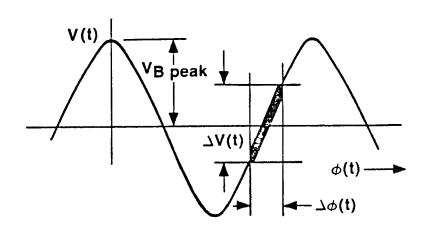
With the LO and RF input in phase quadrature steady state wise, the mixer IF output generates a signal proportional to the phase difference of the two sources. In linear operation the phase detector constant equals the peak voltage of the sinusoidal beat signal produced when either source is frequency offset.

Most sources would drift out of quadrature over the period of measurement. A narrow band phase lock loop automatically forces the two signals into phase quadrature. At rates less than the loop bandwidth the sources now track each other; at rates higher than the loop bandwidth the phase fluctuations are unaffected. The two isolation amplifiers should prevent injection locking of the sources.

Phase noise measured on the IF port represents the rms-sum of the noise contributions of each source. For definite data on the source under test, the phase noise of the reference source should be either negligible or well characterized. In the absence of any information on both sources, one can at least state that neither source noise is worse than the measured data. If 3 unknown sources are available, 3 measurements with 3 different source combinations yield sufficient data to calculate accurately each individual noise level.

The Mixer as Phase Detector 3.1.1





$$V(t) = \underbrace{K_L V_R \cos \left((\omega_R - \omega_L) t + \phi(t) \right)}_{V_B \text{ peak}} + \underbrace{K_L V_R \cos \left((\omega_R + \omega_L) t + \phi(t) \right)}_{+ \dots} + \underbrace{K_L V_R \cos \left((\omega_R + \omega_L) t + \phi(t) \right)}_{+ \dots} + \underbrace{K_L V_R \cos \left((\omega_R + \omega_L) t + \phi(t) \right)}_{+ \dots} + \underbrace{K_L V_R \cos \left((\omega_R + \omega_L) t + \phi(t) \right)}_{+ \dots} + \underbrace{K_L V_R \cos \left((\omega_R + \omega_L) t + \phi(t) \right)}_{+ \dots} + \underbrace{K_L V_R \cos \left((\omega_R + \omega_L) t + \phi(t) \right)}_{+ \dots} + \underbrace{K_L V_R \cos \left((\omega_R + \omega_L) t + \phi(t) \right)}_{+ \dots} + \underbrace{K_L V_R \cos \left((\omega_R + \omega_L) t + \phi(t) \right)}_{+ \dots} + \underbrace{K_L V_R \cos \left((\omega_R + \omega_L) t + \phi(t) \right)}_{+ \dots} + \underbrace{K_L V_R \cos \left((\omega_R + \omega_L) t + \phi(t) \right)}_{+ \dots} + \underbrace{K_L V_R \cos \left((\omega_R + \omega_L) t + \phi(t) \right)}_{+ \dots} + \underbrace{K_L V_R \cos \left((\omega_R + \omega_L) t + \phi(t) \right)}_{+ \dots} + \underbrace{K_L V_R \cos \left((\omega_R + \omega_L) t + \phi(t) \right)}_{+ \dots} + \underbrace{K_L V_R \cos \left((\omega_R + \omega_L) t + \phi(t) \right)}_{+ \dots} + \underbrace{K_L V_R \cos \left((\omega_R + \omega_L) t + \phi(t) \right)}_{+ \dots} + \underbrace{K_L V_R \cos \left((\omega_R + \omega_L) t + \phi(t) \right)}_{+ \dots} + \underbrace{K_L V_R \cos \left((\omega_R + \omega_L) t + \phi(t) \right)}_{+ \dots} + \underbrace{K_L V_R \cos \left((\omega_R + \omega_L) t + \phi(t) \right)}_{+ \dots} + \underbrace{K_L V_R \cos \left((\omega_R + \omega_L) t + \phi(t) \right)}_{+ \dots} + \underbrace{K_L V_R \cos \left((\omega_R + \omega_L) t + \phi(t) \right)}_{+ \dots} + \underbrace{K_L V_R \cos \left((\omega_R + \omega_L) t + \phi(t) \right)}_{+ \dots} + \underbrace{K_L V_R \cos \left((\omega_R + \omega_L) t + \phi(t) \right)}_{+ \dots} + \underbrace{K_L V_R \cos \left((\omega_R + \omega_L) t + \phi(t) \right)}_{+ \dots} + \underbrace{K_L V_R \cos \left((\omega_R + \omega_L) t + \phi(t) \right)}_{+ \dots} + \underbrace{K_L V_R \cos \left((\omega_R + \omega_L) t + \phi(t) \right)}_{+ \dots} + \underbrace{K_L V_R \cos \left((\omega_R + \omega_L) t + \phi(t) \right)}_{+ \dots} + \underbrace{K_L V_R \cos \left((\omega_R + \omega_L) t + \phi(t) \right)}_{+ \dots} + \underbrace{K_L V_R \cos \left((\omega_R + \omega_L) t + \phi(t) \right)}_{+ \dots} + \underbrace{K_L V_R \cos \left((\omega_R + \omega_L) t + \phi(t) \right)}_{+ \dots} + \underbrace{K_L V_R \cos \left((\omega_R + \omega_L) t + \phi(t) \right)}_{+ \dots} + \underbrace{K_L V_R \cos \left((\omega_R + \omega_L) t + \phi(t) \right)}_{+ \dots} + \underbrace{K_L V_R \cos \left((\omega_R + \omega_L) t + \phi(t) \right)}_{+ \dots} + \underbrace{K_L V_R \cos \left((\omega_R + \omega_L) t + \phi(t) \right)}_{+ \dots} + \underbrace{K_L V_R \cos \left((\omega_R + \omega_L) t + \phi(t) \right)}_{+ \dots} + \underbrace{K_L V_R \cos \left((\omega_R + \omega_L) t + \phi(t) \right)}_{+ \dots} + \underbrace{K_L V_R \cos \left((\omega_R + \omega_L) t + \phi(t) \right)}_{+ \dots} + \underbrace{K_L V_R \cos \left((\omega_R + \omega_L) t + \phi(t) \right)}_{+ \dots} + \underbrace{K_L V_R \cos \left((\omega_R + \omega_L) t + \phi(t) \right)}_{+ \dots} + \underbrace{K_L V_R \cos \left((\omega_R + \omega_L) t + \phi(t) \right)}_{+ \dots} + \underbrace{K_L V_R \cos \left((\omega_R + \omega_L) t + \phi(t) \right)}_{+ \dots} + \underbrace{K_L V_R \cos \left((\omega_R + \omega_L) t + \phi(t) \right)}_{+ \dots} + \underbrace{K_L V_R \cos$$

$$V(t) = V_{B peak} \cos ((\omega_{R} - \omega_{L}) t + \phi(t))$$

K_L = Conversion Loss

VB peak [≠] Peak Voltage of **Beat Signal**

for
$$\omega L = \omega R$$
 and $\phi(t) = (k+1) 90^{\circ} + \Delta \phi(t)$

$$\Delta V$$
 (t) = (\pm) VB peak sin $\Delta \phi$ (t)

for $\Delta \phi <<1$

$$\Delta V (t) = (\pm) K_{\phi} \Delta \phi(t)$$
 $K_{\phi} = V_{B peak} \left[\frac{Volts}{rad} \right]$

The frequency conversion of signals applied to a mixer is physically based on one signal gating the other or on the nonlinear conductance of the mixer diodes. Either case represents a multiplying process Among many terms, the mixer also generates a product resulting in the difference frequency of the inpu

With a sinusoidal input at the RF port and assuming linear operation (linear relation between RF and I) voltage, the beat signal is also a sine wave with a peak voltage VB peak. The phase slope of this bea

signal at zero crossings equals ± VB peak.

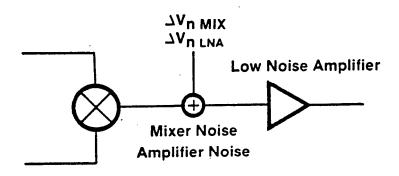
Setting both input signals to the same frequency and at a phase offset of 90° phase fluctuations $\Delta \phi$ result in voltage fluctuations $\Delta v(t)$ which for small phase deviations are linearly related. The mixer acts a a phase detector with a phase detector constant of K_{ϕ} equal to V_{B} peak

3.1.2 Calibration with Beat Signal

Assuming V_{B} peak as the phase detector constant dictates linear operation of the mixer. The calibration of the spectrum analyzer for $\mathcal{L}(f)$ or $S_{\Delta\phi}(f)$ can then be read from the above equations.

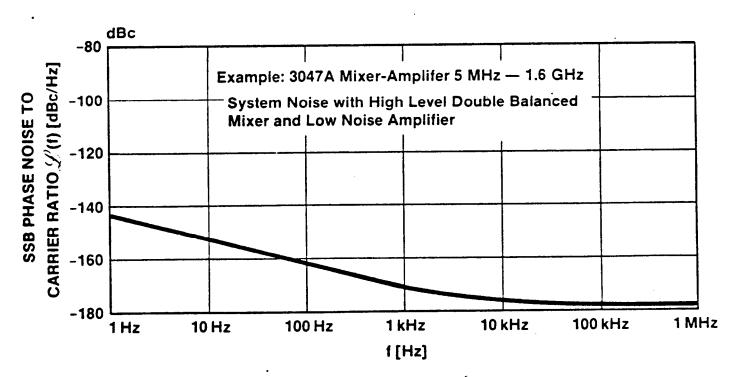
Other calibration methods are based on the characterization of the phase slope, on calibrated PM or FM sidebands on one of the two sources or on the injection of a calibrated spurious signal.

3.1.3 System Noise Floor



$$y = \frac{1}{4} \frac{\Delta V^2 n MIX + \Delta V^2 n LNA}{V^2_B rms}$$

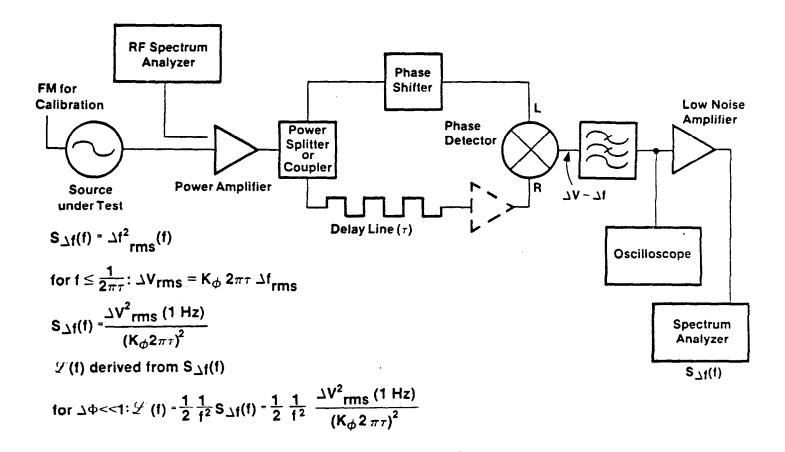
The noise floor of the measurement system is established by the equivalent amplifier and mixer noise (shot noise, flicker noise) at the IF port reduced by the phase detector gain K_{ϕ} (VB peak for linear operation). System sensitivity is therefore maximized by using a high level mixer, selecting a mixer with low flicker noise and following the mixer with a low noise amplifier.



3.1.4 Potential Error Sources

- Suppression or peaking of noise near or below the phase lock loop bandwidth.
- Injection locking
- Deviation from phase quadrature (resulting in an effectively lower K_{ϕ})
- Non-linear operation of mixer (if linearity was assumed in calibration)
- ullet Distortion of RF-Signal (resulting in a deviation of K_ϕ from VB peak)
- Saturation of the preamplifier or spectrum analyzer in calibration or by high spurious signals (e.g. line spurious)
- Change of impedance interface while going from calibration to measurement
- Insufficient cancelation of common source noise in a residual noise test
- AM-noise (in particular AM noise of the common source in a residual noise test)
- Closely spaced spurious (e.g. line multiples) misinterpreted as phase noise
- Noise injected by peripheral circuitry (power supply, phase lock loop)
- Noise injected by peripheral instrumentation (e.g., oscilloscope, DVM)
- Microphonic noise.

3.2 Delay Line Frequency Discriminator Technique

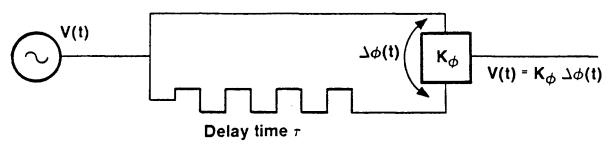


A delay line and a mixer operating as a phase detector have the combined effect of a frequency discriminator. The delay line transforms any frequency fluctuation into a phase fluctuation and the mixer with the L and R inputs at 90° offset linearly converts the phase fluctuations into voltage fluctuations at the IF port. As derived on the next page, the equivalent frequency discriminator constant K_D is proportional to τ and K_{ϕ} .

The frequency to phase conversion has a sin x/x characteristic with a null at $f = 1/\tau$. Measurements are therefore limited to $f < 1/2\tau$ (with correction).

The system can be calibrated by characterizing the frequency slope or K_{ϕ} and τ , or more conveniently by adding FM sidebands to the source under test (or a substitute) and determining their level as reference level with an RF spectrum analyzer.

3.2.1 Theory of Delay Line Frequency Discriminator



$$f(t) = f_0 + \Delta f \sin 2\pi f t$$

$$V(t) = V_0 \cos (2\pi t_0 t - \frac{\Delta f}{t} \cos 2\pi f t)$$

Carrier with sinusoidal FM f_o = Carrier frequency f = FM rate Δf = FM deviation

Phase Difference on Phase Detector $\Delta \phi(t)$

Phase Detector Output: $V(t) = K_{\phi} \Delta \phi(t)$ for $2\pi f_{0}\tau = (k+1)\frac{\pi}{2}$ (Phase Quadrature): $V(t) = K_{\phi} 2\frac{\Delta f}{f} \sin \pi f \tau \sin 2\pi f (t - \frac{\tau}{2})$

Amplitude Response:

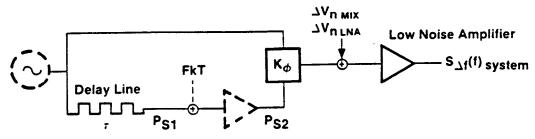
$$\Delta V = K_{\phi} 2 \frac{\Delta f}{f} \sin \pi f \tau = K_{\phi} 2\pi \tau \Delta f \frac{\sin \pi f \tau}{\pi f \tau}$$

for
$$f \le \frac{1}{2\pi\tau}$$
: $\frac{\sin \pi f \tau}{\pi f \tau} \approx 1$

$$\Delta V = K_{\phi} 2\pi \tau \Delta f$$

$$K_D$$
 $\left[\begin{array}{c} V \\ Hz \end{array}\right]$ = Frequency Discriminator Constant = K_ϕ $2\pi\tau$

3.2.2 System Noise Floor



Phase Noise of RF Amplifier (if used):
$$\Delta\theta^2 RF = \frac{FkT}{PS1} (1 + \frac{f}{fc})$$

 K_{ϕ} (in linear operation): $K_{\phi} = V_{B peak} = \sqrt{2} P_{S2} L \times 50\Omega$
with L = Conversion Loss (Power) of Mixer

Mixer noise and baseband amplifier noise are interpreted by the system as frequency noise. This also holds for phase noise introduced by an RF amplifier. The amplifier might have been added to boost the signal level lowered by the lossy delay line.

$$s_{\Delta f}(f) = \frac{\Delta V_{\text{n MIX}}^{2}}{(K_{\phi} 2\pi\tau)^{2}} + \frac{\Delta V_{\text{n LNA}}^{2}}{(K_{\phi} 2\pi\tau)^{2}} + \frac{\Delta^{(c)^{2}}RF}{(2\pi\tau)^{2}}$$

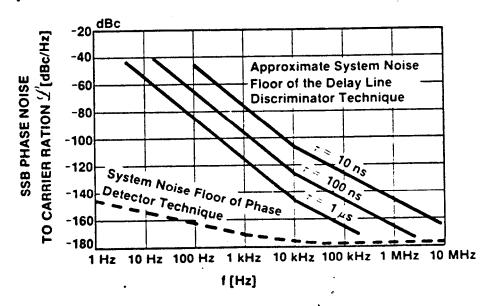
$$\mathscr{L}'(f) = \frac{1}{2} \frac{1}{f^{2}} s_{\Delta f}(f) \qquad \text{for } \Delta\phi <<1$$

$$system$$

$$\mathscr{L}'(f) = \frac{1}{2} \left(\frac{1}{2\pi f\tau}\right)^{2} \left(\frac{\Delta V_{\text{n MIX}}^{2}}{K_{\phi}^{2}} + \frac{\Delta V_{\text{n LNA}}^{2}}{K_{\phi}^{2}} + \Delta^{(c)^{2}}RF\right)$$
system

Mixer, baseband amplifier and RF amplifier noise all have a flicker noise characteristic close in and show white noise farther out. The contributions of mixer and baseband amplifier are reduced by K_{ϕ} which points out the benefit of the RF amplifier as it maximizes K_{ϕ} . A better solution, though, is to employ a power amplifier before the signal split and eliminate the need for an RF amplifier and with it $\Delta \Theta_{RF}$.

Since $S_{\Delta f}$ has a flicker and then white noise characteristic, L(f) increases with f^{-2} and f^{-3} towards the carrier. This means good sensitivity far out (limited by the sinx/x response) but poor sensitivity as compared with the phase detector method close in.

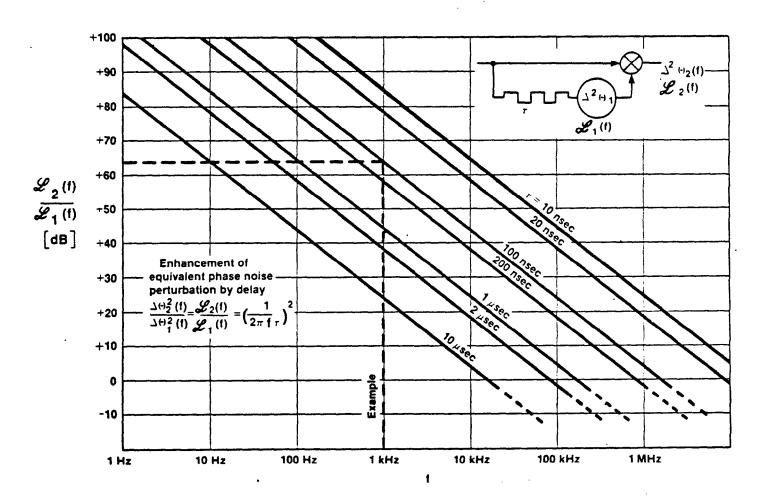


A different way of estimating the system noise floor of the delay line discriminator technique is to lump all noise contributions into an equivalent phase noise perturbation $\Delta\Theta^2_1$ (or \mathcal{L}_1). The delay line discriminator-interpreting $\Delta\Theta$ as frequency noise enhances this equivalent phase noise by a factor of $1/2\pi f\tau$.

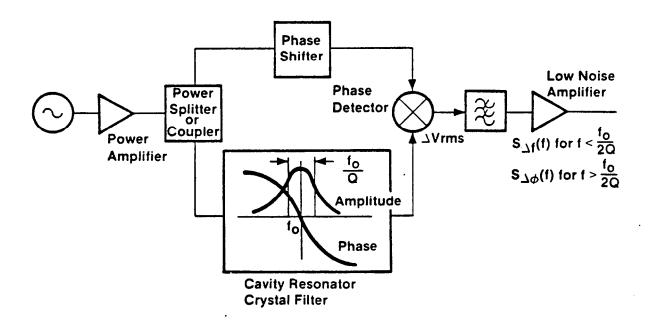
Example:

Phase noise of the mixer at 1 kHz \mathcal{L}_1 (1 kHz) = -160 dBc/Hz. With a 100 nsec delay time the resulting system noise floor is

$$\mathcal{L}_2 = \mathcal{L}_1 \left(\frac{1}{2\pi f \tau} \right)^2$$
.
= -160 dBc + 64 dB = -96 dBc/Hz



3.2.3 Frequency Discriminator with Resonator



Inside Resonator Bandwidth (f < $\frac{f_0}{2Q}$):

Equivalent Delay Time
$$\tau = \frac{d\phi}{d\omega}\Big|_{f_0} = \frac{Q}{\pi f_0}$$

$$K_{D} = K_{\phi} 2\pi\tau = K_{\phi} \frac{2Q}{f_{o}}$$

$$\Delta V_{rms}(f) = K_{\phi} \frac{2Q}{f_0} \Delta f_{rms}(f)$$

Although not a broadband solution, it can be advantageous to replace the delay line with a cavity resonator or crystal filter. Group delay $(d\phi/d\omega)$ produces an equivalent τ of $Q/\pi f_0$ for Fourier frequencies less than half the bandwidth. Inside $f_0/2Q$, the system therefore measures the spectral density of frequency noise with a discriminator constant of $K_{\phi}/2Q/f_0$.

Outside Resonator Bandwidth (f > $\frac{f_0}{2Q}$):

$$\Delta V_{rms}(f) = K_{\phi} \Delta \phi_{rms}(f)$$

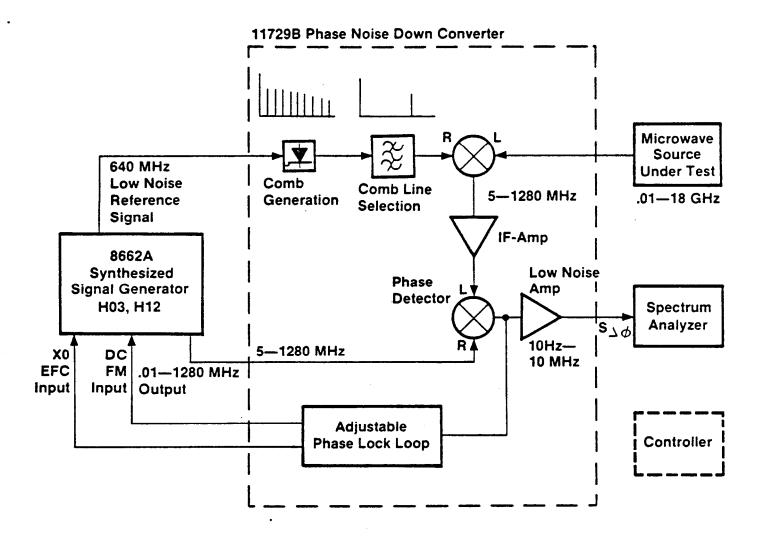
Outside $f_0/2Q$, the system may be viewed as 2 signals applied to a phase detector with the sideband noise of one signal stripped off by the cavity resonator. The system then measures $\Delta \phi^2_{rms}$ with a phase detector constant K_{ϕ} .

4. HP Phase Noise Measurement Solutions

- 11729B Phase Noise Test System
- 3047A Spectrum Analyzer System
- 3047A/11729B Phase Noise Test System
- 5390A Frequency Stability Analyzer

Following is a comparison of HP phase noise measurement solutions with particular focus on the 11729B Phase Noise Down Converter, HP's most recent contribution to phase noise measurement.

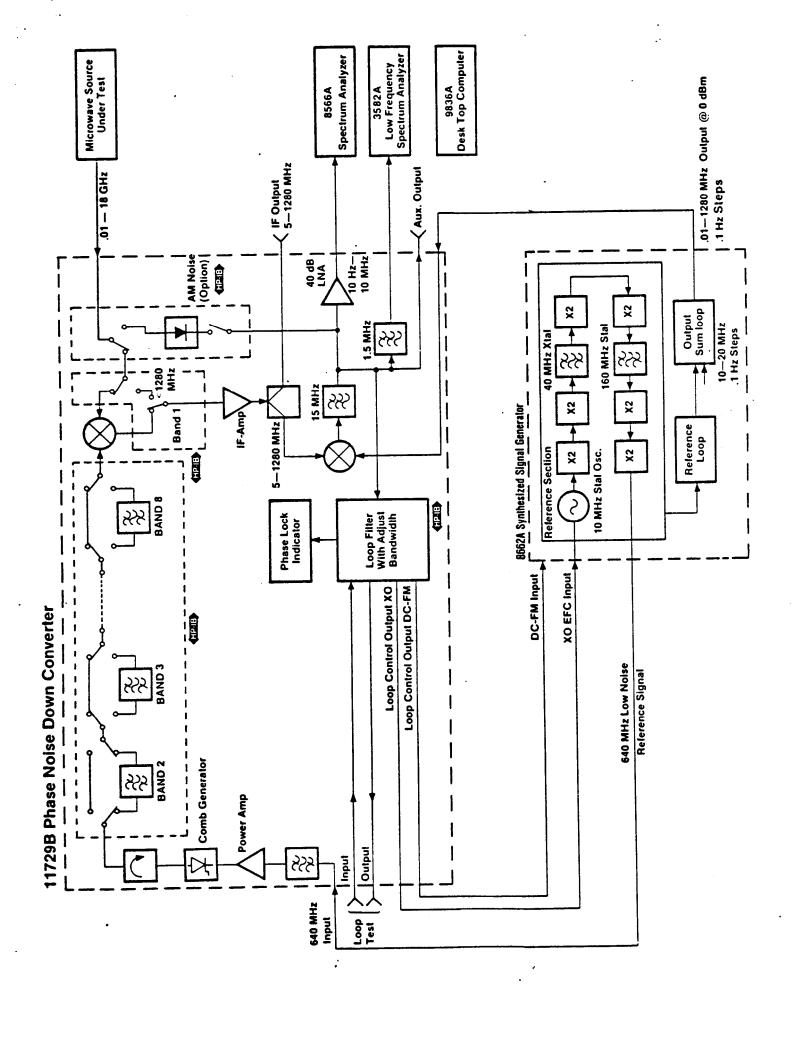
4.1 11729B Phase Noise Test System Principle of Operation



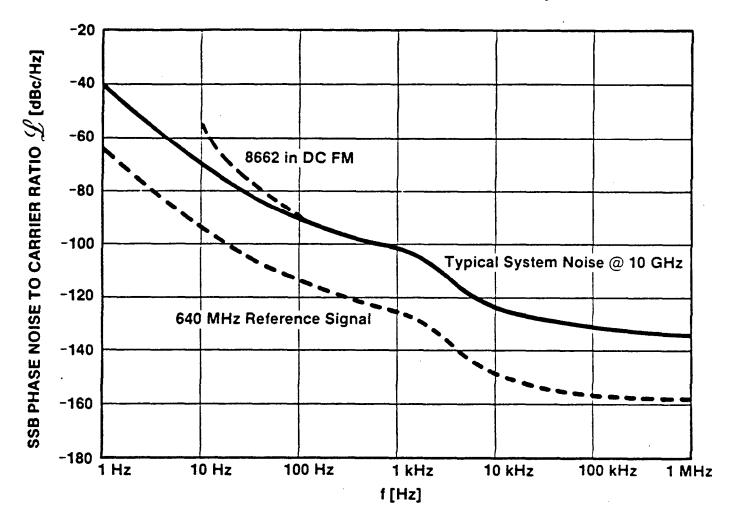
The 8662A Synthesized Signal Generator provides a state of the art low noise reference signal at 640 MHz to the 11729B Phase Noise Down Converter. This signal is applied to a step recovery diode multiplier which generates a comb of signals spaced by 640 MHz ranging beyond 18 GHz. A switched in filter selects an appropriate combline. This microwave reference signal down converts the microwave source under test into an IF range of 5 to 1280 MHz.

The double balanced mixer phase compares the IF signal with the output signal of the 8662A 0.01 to 1280 MHz). Phase quadrature is enforced by phase locking the 8662A to the signal under test either thru the limited electronic control of the 8662A's 10 MHz crystal oscillator or thru its DC-FM port. The loop bandwidths can be set over a 4 decade range.

A first order phase lock loop is enabled by the capture feature making lock acquisition easy. Then the second order phase lock loop (with very high dc gain) maintains lock and phase quadrature. To enable phase noise measurements within the loop bandwidth, the phase noise suppression can be characterized via the loop test ports.



4.1.1 Noise Floor of 11729B Phase Noise Test System



$$y_{\text{system}} = 10 \log \left(N^2 \times 10^{\frac{10}{10}} - 10^{\frac{y_2}{10}} - 10^{\frac{y_3}{10}} \right)$$

N = Multiplication number of the 640 MHz signal

7 1 = Absolute SSB Phase Noise of the 640 MHz reference signal (dBc/Hz).

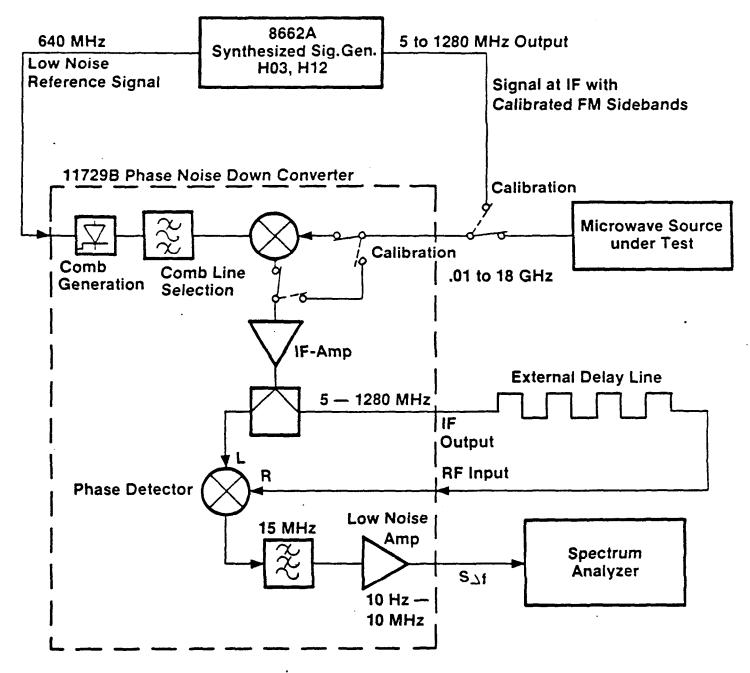
2 = Absolute SSB Phase Noise of the 5 to 640 (1280) MHz tunable signal (dBc/Hz).

3 = Residual noise of 11729B (dBc/Hz).

Offset from carrier	Typical 640 MHz low noise output	Typical 320-640 MHz tun- able 8662A output	Typical Residual noise of 11729B @ 5 GHz	Typical Residual noise of 11729B @ 10 GHz
1 Hz	-64 dBc	-64 dBc	-99 dBc	-93 dBc
10 Hz	-94 dBc	-94 dBc	-112 dBc	-106 dBc
100 Hz	-114 dBc	-114 dBc	-125 dBc	-119 dBc
1 kHz	-126 dBc	-125 dBc	-132 dBc	-126 dBc
10 kHz	-149 dBc	-136 dBc	-137 dBc	-131 dBc
100 kHz	-159 dBc	-136 dBc	-147 dBc	-141 dBc
1 MHz	-159 dBc	-146 dBc	-149 dBc	-143 dBc

For most microwave applications, \mathcal{I}_1 , the SSB phase noise of the 640 MHz reference signal, is the dominating factor in setting the noise floor of the system as it's phase noise is multiplied up to microwave frequencies..

.4.1.2 11729B Operating with Delay Line



- Low noise down conversion of microwave signal under test
- Delay line operating at IF (5—1500 MHz)
- Calibration signal provided by 8662A
- Phase quadrature indication.

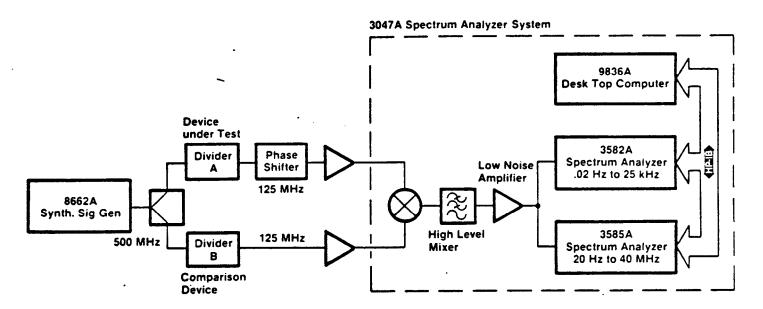
The 11729B may also be used in a delay line mode. The microwave reference signal generated by the 640 MHz signal down converts the source under test into the IF range. The power splitter and auxiliary IF output allows one to simply insert an appropriate delay line externally.

Delaying the signal at IF means lower delay line losses and also a wider choice of delay devices, like e.g. SAW delay line. The 8662A with FM sidebands and set to the IF signal frequency is a convenient calibration tool.

3047A Spectrum Analyzer System

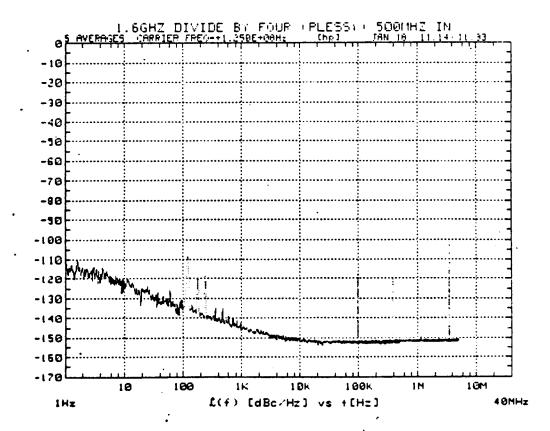
Application Example:

Residual phase noise measurement on frequency divider

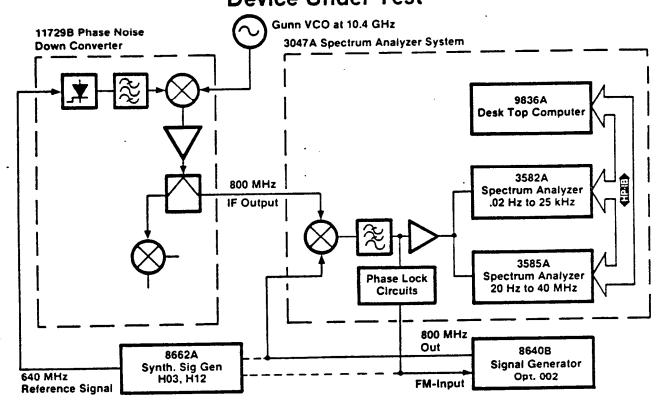


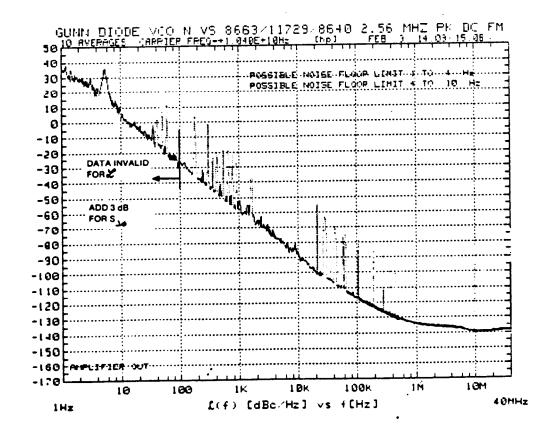
The 3047A Spectrum Analyzer System represents a high sensitivity phase detector system with controllable phase lock circuits. Extensive software automates the calibration and measurement procedures.

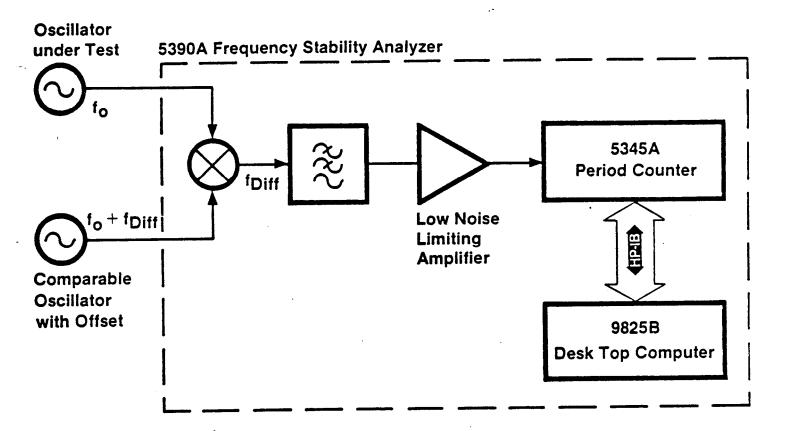
Determining the phase noise of frequency dividers is an example of a residual phase noise measurement. Here, the phase noise of the 8662 is largely canceled and the system will measure the rms sum of noise of both dividers.



4.3 3047A/11729B Phase Noise Test System Device Under Test



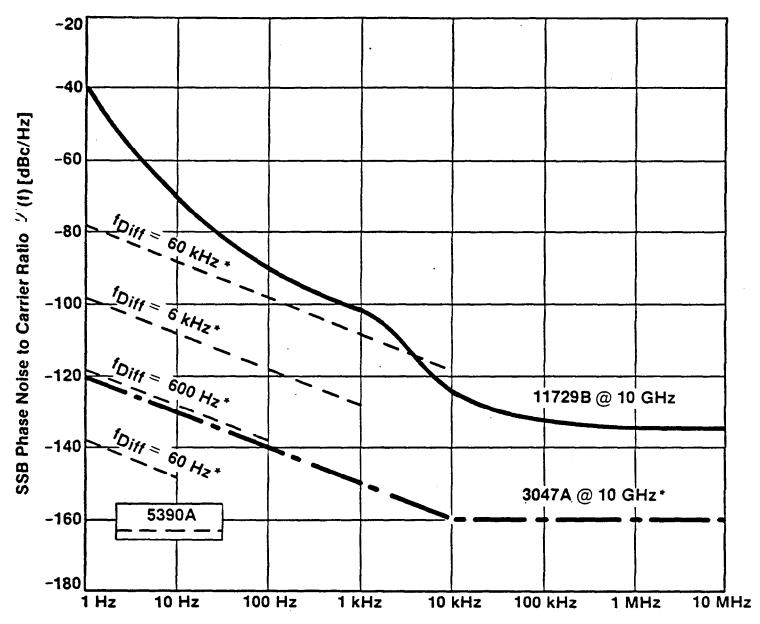




 \mathcal{L} system (f) = -173 + 20 log fDiff - 10 log f

The 5390A Frequency Stability Analyzer measures frequency noise in the time domain (Allan Variance by means of a period counter. To achieve sufficient resolution, heterodyning is a necessity and requires a second comparable source offset by the desired difference frequency.

The 9825B converts the time domain data into spectral densities. The 5390A has a very impressive noise floor close in, e.g., <-140 dBc/Hz at 10 Hz with a difference frequency of 60 Hz. Beyond 100 Hz, it quickly loses sensitivity compared to other methods.



^{*}Performance shown requires a second, user supplied source of comparable phase noise.

4.6 Phase Noise Measurement Systems Comparison

	11729B Phase Noise Test System	3047A Spectrum Analyzer System	3047A/11729B Phase Noise Test System	5390A Frequency Stability Analyzer	
	8566A 8568A 3585A 3582A Spectrum Analyzers 11729B Phase Noise Down Converter 8662A Synthesized Sig Gen	3585A Spectrum Analyzer 3582A Spectrum Analyzer 35601A S.A. Interface	3585A Spectrum Analyzer 3582A Spectrum Analyzer 35601A S.A. Interface 11729B Phase Noise Down Converter 8662A Synthesized Sig Gen	5345A Counter 5358A Meas. Stor. Plug-in 10830A Mixer, IF Amp.	
	Controller	9845B or 9836A	9836A Controller	9825B Controller	
Principle of Operation	Source under Test	Source Under Test Comparable Source	Source Under Test	Source Under Test Counter Comparable Source w/offset	
Frequency Range	.005 — 18 GHz	.005 — 18 GHz	.005 — 18 GHz	500 kHz — 18 GHz	
Low Noise Microwave Reference Signal	yes	. no	yes	no	
System Noise Spec at 10 GHz	-120 dBc at 10 kHz -132 dBc noise floor	-160 dBc at 10 kHz* -160 dBc noise floor	-120 dBc at 10 kHz -132 dBc noise floor (with 11729B)	-140 dBc at 1 Hz* -126 dBc at 1 kHz	
Noise Spectrum	<10 MHz	<40 MHz	<10 MHz (<40 MHz)	<10 kHz	
Residual Noise (Measurement of Amplif., Dividers)	no	yes	yes	no	
Delay Line Mode	yes	yes	yes	no	
AM-Noise (Microwave)	yes	w/external detector	yes	no	
Manual Operation	yes	no	yes	interactive applicatio program	
Automatic Operation (HP-IB)	yes	yes	yes .	yes	
Software	planned	extensive	extensive for 3047 planned for 11729	yes	

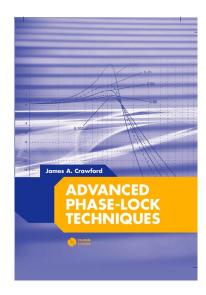
^{*}Specification shown requires a second, user supplied source of comparable phase noise.

GLOSSARY OF SYMBOLS

FkTB	[mW]	Available noise power in bandwidth B
fo	[Hz]	Carrier frequency
fc	[Hz]	Corner frequency of flicker noise
f	[Hz]	Fourier frequency (sideband-, offset-, modulation, baseband-frequency)
f(t)	[Hz]	Instantaneous frequency
∆f(t)	[Hz]	Instantaneous frequency fluctuation
$K_{oldsymbol{\phi}}$	[volt/rad]	Phase detector constant
KD	[volt/Hz]	Frequecy discriminator constant
\mathscr{L} (f)	[dBc/Hz]	Ratio of single sideband phase noise to total signal
		power in a 1 Hz bandwidth f Hertz from the carrier
Ps	[mW]	Signal power
Pssb	[mW]	Power of single sideband
Q		Quality factor of resonator
S _1(f)	[Hz²/Hz]	Spectral density of frequency fluctuations
$S_{\Delta\phi}(f)$	[rad ² /Hz]	Spectral density of phase noise
t	[sec]	Time
v(t)	[volt]	Instantaneous voitage
V _B peak	[volt]	Peak voltage of sinusoidal beat signal
$\Delta \phi(t)$	[rad]	Instantaneous phase fluctuation
$\Delta\Theta(t)$	[rad]	Instantaneous phase perturbance
τ	[sec]	Delay time

REFERENCES

- 1) Ashley, R.J., et al, "The Measurement of Noise in Microwave Transmitters," *IEEE Transactions on Microwave Theory and Techniques*, Vol. MTT-25, No. 4, pp. 294-318, (April, 1977).
- 2) Fischer, M., "An Overview of Modern Techniques for Measuring Spectral Purity", Microwaves, Vol. 18, No. 7, pp. 66-75, July 1979.
- 3) Hewlett-Packard staff, "Understanding and Measuring Phase Noise in the Frequency Domain," Application Note 207, October 1976.
- 4) Hewlett-Packard staff, "Measuring Phase Noise with the HP 3585A Spectrum Analyzer", Application Note 246-2, May 1981.
- 5) Hewlett-Packard staff, "Applications and Measurements of Low Phase Noise Signals Using the 8662A Synthesized Signal Generator", Application Note 283-1, November 1981.
- 6) Howe, D.A., "Frequency Domain Stability Measurements: A Tutorial Introduction", NBS Technical Note 679, March 1976.
- 7) Lance, A.L., Seal, W.D., Hudson, N.W., Mendoza, F.G., and Donald Halford, "Phase Noise Measurements Using Cross-Spectrum Analysis," Conference on Precision Electromagnetic Measurements, Ottawa, Canada, June 1978.
- 8) Ondria F.G., "A Microwave System for Measurements of AM and FM Noise Spectra", IEEE Transactions on Microwave Theory and Techniques, Vol. MTT-16, pp. 767-781, September 1968.
- 9) Scherer, Dieter, "Design Principles and Test Methods for Low Phase Noise RF and Microwave Sources", Hewlett-Packard RF and Microwave Measurement Symposium, October 1978. (Also edited version in Microwaves, "Today's Lesson Learn about Low-Noise Design", part 1, pp. 116-122, April 1979, part 2, pp. 72-77, May 1979).
- 10) Shoaf, J.H., Halford, D., Risley, A.S., "Frequency Stability Specification and Measurement: High Frequency and Microwave Signals", NBS Technical Note 632, January 1973.
- 11) Temple, R., "Choosing a Phase Noise Measurement Technique Concepts and Implementation", Hewlett-Packard RF and Microwave Measurement Symposium, February 1983.



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Chapter	Brief Description	Pages
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	An expansive, multi-disciplined view of the PLL, its history, and its wide application.	
2	Design Notes	44
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	which have not been published before.	
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	Criterion, Poisson Sum, Time-Bandwidth Product.	
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	attention to 1/f noise, and the creation of custom noise sources that exhibit specific power	
	spectral densities.	
5	System Performance	48
	A detailed look at phase noise and clock-jitter, and their effects on system performance.	
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	QAM, M-PSK. Relationships between EVM and image suppression are presented for the first	
	time. The effect of phase noise on channel capacity and channel cutoff rate are also	
	developed.	74
6	Fundamental Concepts for Continuous-Time Systems	71
	A thorough examination of the classical continuous-time PLL up through 4 th -order. The	
	powerful Haggai constant phase-margin architecture is presented along with the type-3 PLL.	
	Pseudo-continuous PLL systems (the most common PLL type in use today) are examined rigorously. Transient response calculation methods, 9 in total, are discussed in detail.	
7	Fundamental Concepts for Sampled-Data Control Systems	32
-	A thorough discussion of sampling effects in continuous-time systems is developed in terms	32
	of the z -transform, and closed-form results given through 4^{th} -order.	
8	Fractional-N Frequency Synthesizers	54
0	A historic look at the fractional-N frequency synthesis method based on the U.S. patent	54
	record is first presented, followed by a thorough treatment of the concept based on Δ - Σ	
	methods.	
9	Oscillators	62
9	An exhaustive look at oscillator fundamentals, configurations, and their use in PLL systems.	02
10	Clock and Data Recovery	52
10	Bit synchronization and clock recovery are developed in rigorous terms and compared to the	52
	I DIL SYNCHIONIZATION AND CIOCK RECOVERY ARE DEVELOPED IN HUDIOUS TERMS AND COMBATED TO THE	1