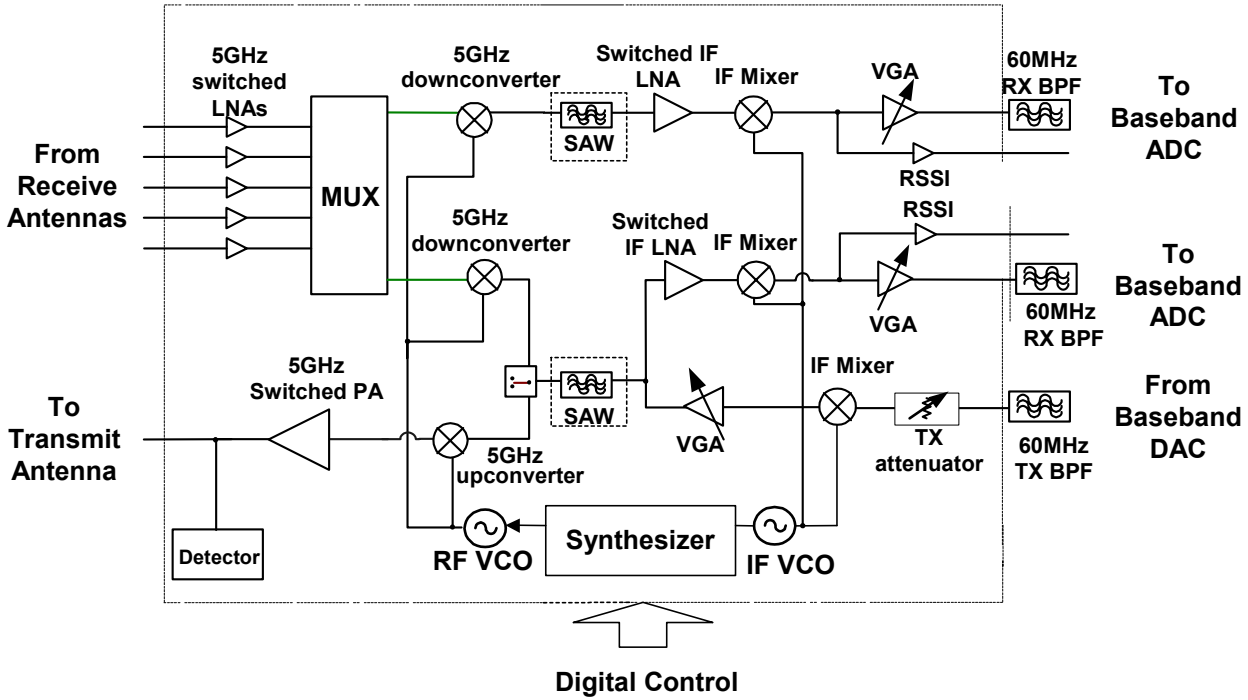

Figure 1 Block diagram of SiGe BiCMOS transceiver RFIC.

Power Supply	**3.3V**
Integrated Phase Noise	**1degree RMS**
RX Noise Figure	**4.5dB**
Max RX Gain	**88dB**
RX Input IP3	**-12dBm**
TX Output Power	**18dBm**
Max TX Gain	**46dB**
Technology	**SiGe BiCMOS**
Package	**97 pin LGA**
Die Size	**25mm^2**

Figure 2: Summary of RF transceiver performance.

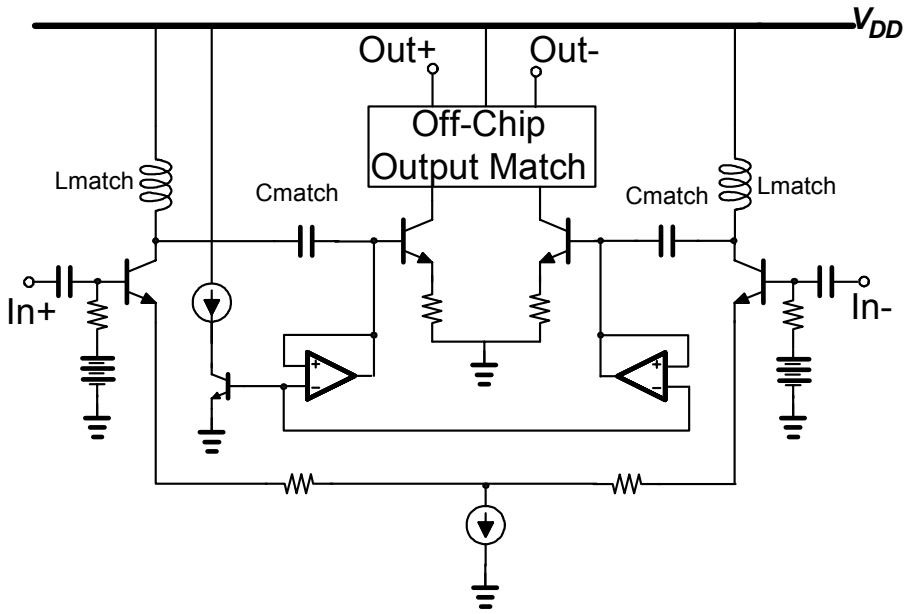

Fig 3: High power output stage and power amplifier driver.

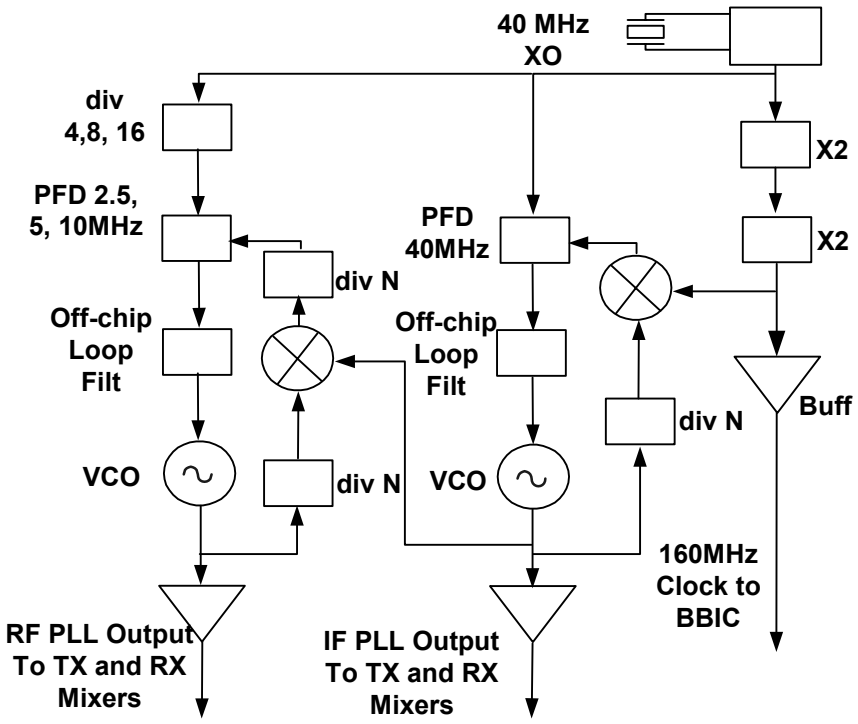

Fig 4: Block diagram of frequency synthesizer.

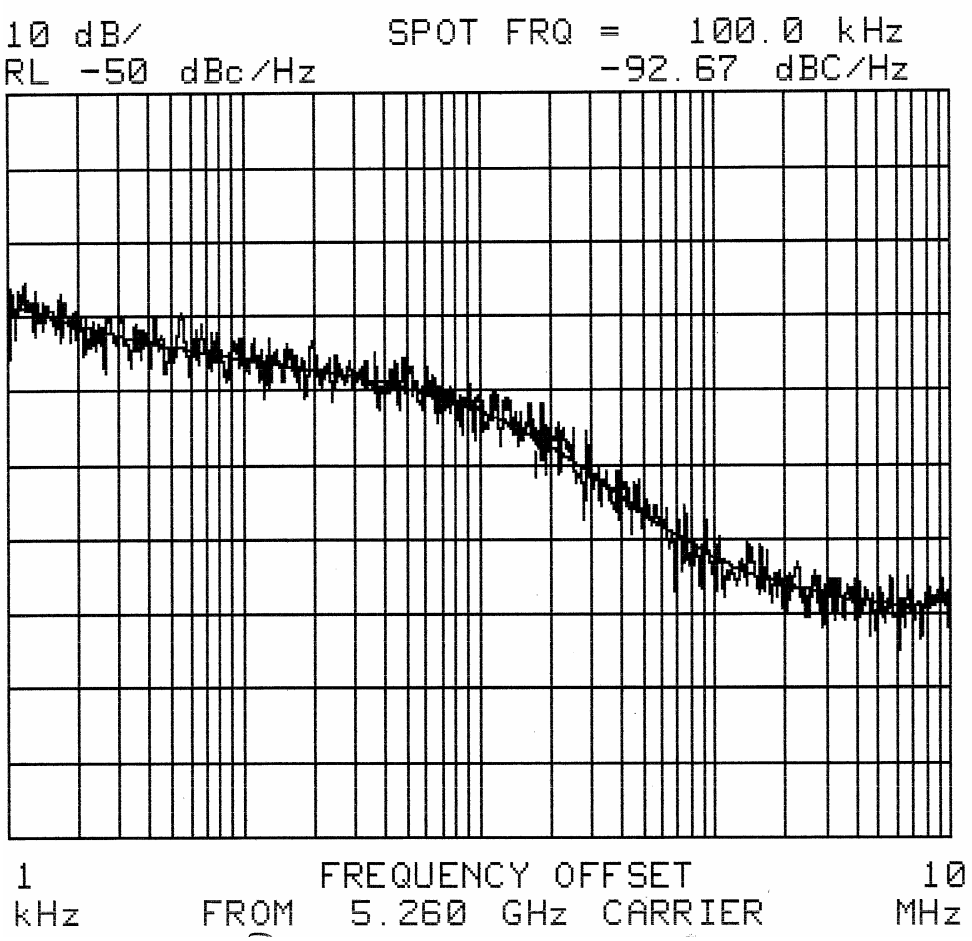

Figure 5: Phase noise plot at transmitter output

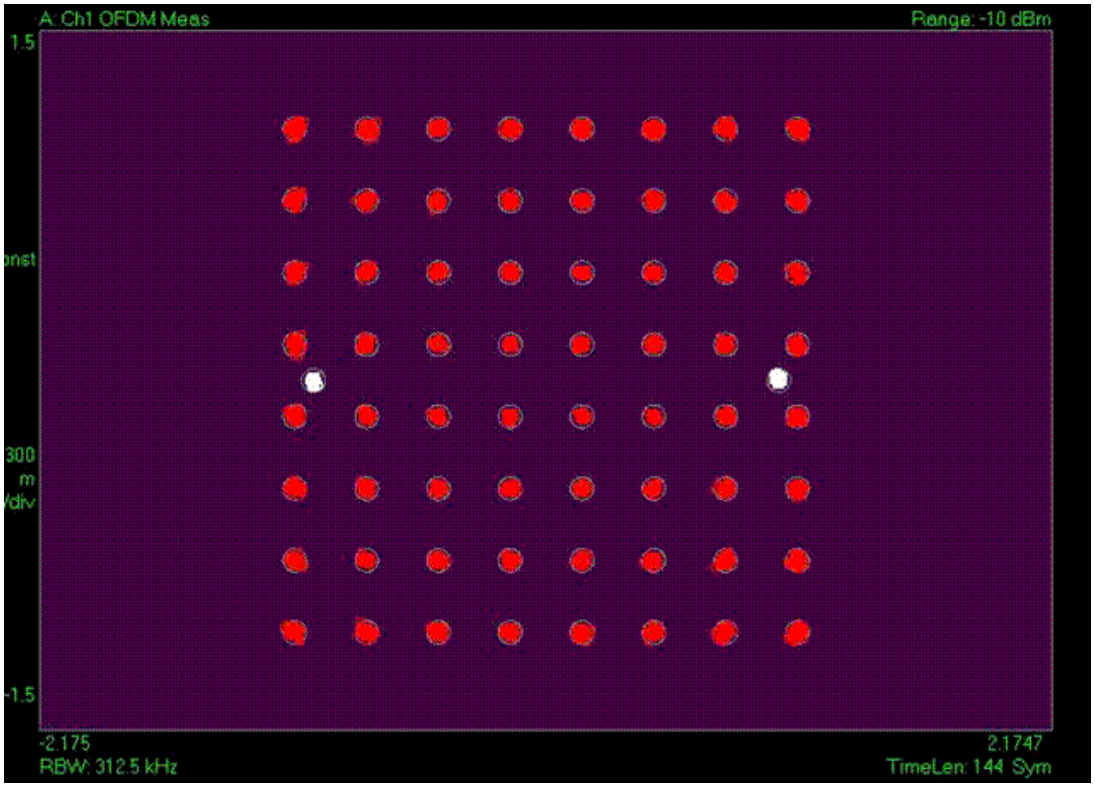

Fig 5: RFIC 64 QAM transmit constellation.

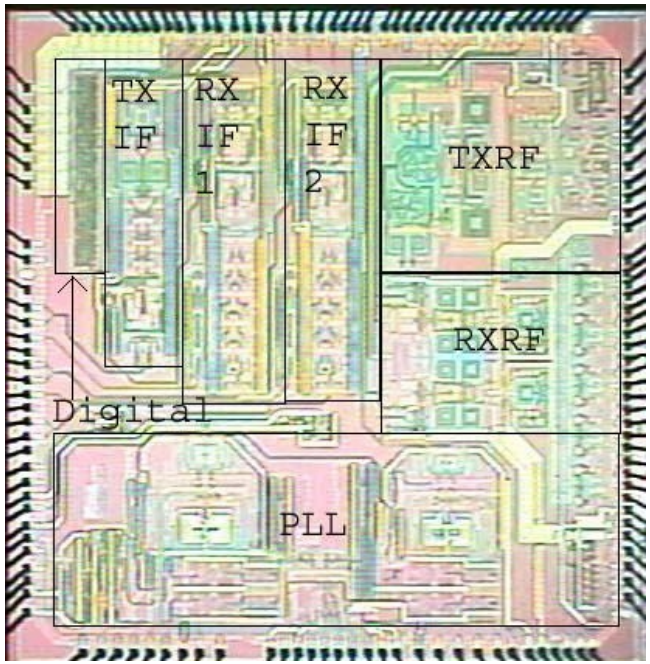

Figure 6: Die photo of SiGe BiCMOS transceiver RFIC.