## Frequency Synthesizer Design Handbook Errata

Page 49, Equation 3.22

$$Y\left(e^{j2\pi f T_s}\right) \approx \frac{1}{T_s} X\left(f\right) for \left|f T_s\right| \leq \frac{1}{2}$$

Page 70, Equation 3.62

$$\begin{bmatrix} \phi_0 & \phi_1 & \phi_2 & \dots & \phi_M \\ \phi_1 & \phi_0 & \phi_1 & \dots & \phi_{M-1} \\ \dots & \dots & \dots & \dots & \dots \\ \phi_M & \phi_{M-1} & \phi_{M-2} & \dots & \phi_0 \end{bmatrix} \begin{bmatrix} 1 \\ a_1 \\ \dots \\ a_M \end{bmatrix} = \begin{bmatrix} a_0 \\ 0 \\ \dots \\ 0 \end{bmatrix}$$

Page 74, Figure 3.18

Captions should read "-3.2V" rather than "-320 V"

Page 83, Figure 3.28

Loads connected to grounds (2 of them) labeled "S" should be labeled "Z"

Page 85, Equation 3.71

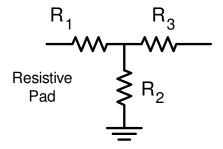
$$G_{T} = \frac{\left|S_{21}\right|^{2} \left(1 - \left|\Gamma_{s}\right|^{2}\right) \left(1 - \left|\Gamma_{L}\right|^{2}\right)}{\left|\left(1 - S_{11}\Gamma_{s}\right) \left(1 - S_{22}\Gamma_{L}\right) - S_{12}S_{21}\Gamma_{s}\Gamma_{L}\right|^{2}}$$

Page 88, Figure 3.31

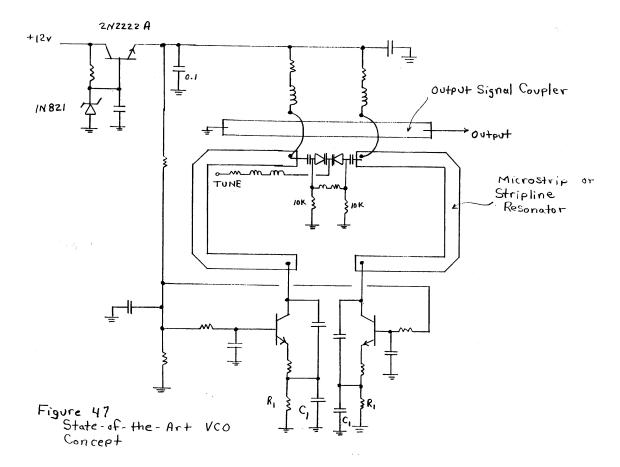
The positive node of the op-amp should be connected directly to ground rather than through a capacitor to ground.

Page 110, Figure 3.46

One entire graphic-inset is missing:



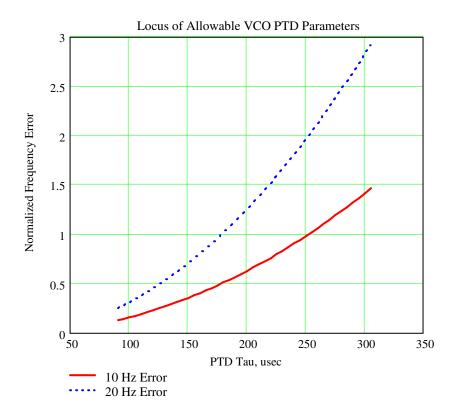
Page 113, Figure 3.47 (Decoupling around varactor diodes shown incompletely)



Page 272, Equation (6.22) The square on sin() was erroneously dropped inside the sin function.

$$E\{\} = \sum_{k=-M}^{+M} \sum_{p=-M}^{+M} R_d (|k-p|) \frac{\sin^2 (\pi f T_s)}{(\pi f)^2} e^{-j2\pi f(k-p)T_s}$$

Page 284, Figure 6.33 Legends on plot were dropped



The normalization referred to for the y-axis makes it very easy to do the plot. Using equation 6.33, the actual quantity plotted in this figure is given by

$$\Delta F e^{-\frac{T_o}{\tau}} = \Delta f \left(T_o\right) \left[ \left(\omega_n \tau\right)^2 + 1 - 2\zeta \omega_n \tau \right]$$

with an additional scaling factor of  $10^{\text{-}6}$  where  $T_o$  is the settling time of interest which was taken to be 500 µsec.

Page 291, Figure 7.2

Graph labeling should read "Attenuation characteristic of filter F<sub>1</sub>"

Page 298

Fourth line down of text should read  $\tau_0$ = 0.125 (subscript left off).

Page 301

Fifth line down of text should read  $\tau_0$  (subscript left off).

Page 357, equation 8.13

Left hand side of the equation should read  $\begin{bmatrix} s_1(t+h) \\ s_2(t+h) \end{bmatrix}$ 

Right hand side first matrix has an h2 that should be simply an h

$$\begin{bmatrix} 1 & -h\omega_n^2 \\ h & (1+2\zeta\omega_n h) \end{bmatrix}$$

Page 367, equation 8.39. The first line is missing a factor of h. Should read as:

$$x_{n+1} = \frac{4}{3}x_n - \frac{1}{3}x_{n-1} + \frac{2}{3}h\dot{x}_{n+1}$$

Page 391, Figure 9.4

Caption should read "with permission" rather than "by permission"

Page 392, Figure 9.5

Caption should read "with permission" rather than "by permission"

## Chapter 5 References

The references listed for Chapter 5 are not correctly listed. Items that appear in the book as "Selected Biography" were removed from the original References list without properly renumbering them in the text. The listing below has the reference numbers that correspond to the numbers used in the main text of Chapter 5.

## References

- 1] Crawford, J.A., "Understanding the Specifics of Sampling in Synthesis," Microwaves and RF, Vol. 23, No. 8, August 1984, pp. 120-126,144
- 2] Barab,S. A.L. McBride, "Uniform Sampling Analysis of a Hybrid Phase-Locked Loop with a Sample-and-Hold Phase Detector," IEEE Trans. AES, Vol. AES-11, No. 2, March 1975, pp. 210-216
- 3] Franklin, G.F., J.D. Powell, <u>Digital Control of Dynamic Systems</u>, Reading,MA: Addison-Wesley Publishing, 1980, pp.79-80
- 4] Kuo, B.C., <u>Digital Control Systems</u>, New York, NY: Holt, Rinehart, Winston, 1980, pp. 52-55, 114-120
- Underhill, M.J., R.I.H. Scott, "The Effect of the Sampling Action of Phase Comparators on Frequency Synthesizer Performance," Proc. 33rd Annual Frequency Control Symposium, 1979, pp. 449-457
- 6] Chua,L.O., P.Lin, <u>Computer-Aided Analysis of Electronic Circuits: Algorithms and Computational Techniques</u>, Englewood Cliffs,NJ: Prentice-Hall, 1975, Ch. 13
- 8] Blake, J., "Design of Wideband Frequency Synthesizers," RF Design, May 1988, pp. 26-34
- 9] Egan, W.F., "The Effects of Small Contaminating Signals in Nonlinear Elements Used in Frequency Synthesis and Conversion," Proc. IEEE, Vol. 69, No. 7, July 1981, pp. 797-811

10] Peregrino, "Phase Noise Measurement Using a High Resolution Counter with On-Line Data Processing," Proc. 30th Annu. Symp. Frequency Control, Atlantic City, NJ, June 1976, pp. 309-317

- Bennett, "Methods of Solving Noise Problems", Proc. IRE, May, 1956, pp. 609-638
- 12] Gardner, W.A., <u>Introduction to Random Processes</u>, 2nd ed., New York, NY: McGraw-Hill Book, 1990
- Papoulis, A., <u>Probability, Random Variable, and Stochastic Processes</u>, New York, NY: McGraw-Hill Book, 1965
- 14] Marple, S.L., Digital Spectral Analysis, Englewood Cliffs, NJ: Prentice-Hall, 1987
- Wheatley, C.E., D.E. Phillips, "Spurious Suppression in Direct Digital Synthesizers," Proc. 35th Annu. Frequency Control Symp., Ft. Monmouth, NJ, May 1981, pp. 428-435
- Blachman, N., <u>Noise and its Effect on Communication</u>, 2nd ed., Malabar,FL: Robert E. Kreiger Publishing, 1982
- 17] Crawford, J.A., "Extending Sampling to Type 2 Phase-Locked Loops", Microwaves & RF, Sept. 1984, pp. 171-174